## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Application Serial No	10/050,348
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Inventor	Gurtej S. Sandhu
Assignee	
Group Art Unit	
Examiner	
Attorney's Docket No	
Title: Transistor Structures	

## SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT

## References - See Attached Form PTO/SB/08a

The attached form PTO/SB/08a is submitted in compliance with 37 CFR § 1.56. Copies of the cited art are included with the exception of U.S. patents and published U.S. applications (1273 Off. Gaz. Pat. Off. 55, 05 August 2003). No admission is made regarding whether all the submitted references are prior art.

Respectfully submitted,

Dated: Murch 16, 2007

By: Sennifer J. Taylor, Ph.D.

Reg. No. 48,711